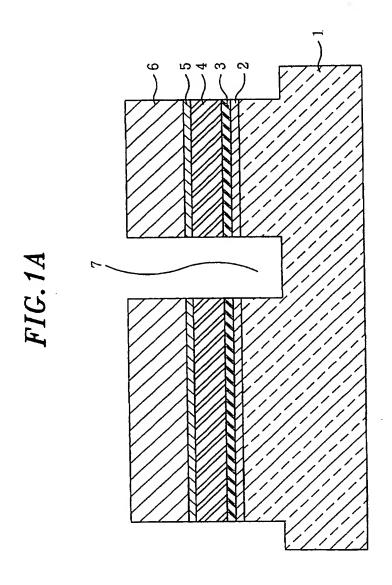
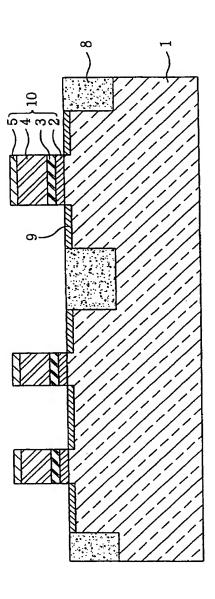
Inventor: Cheolsoo PARK
For: "Methods of Manufacturing Transistors
Using Dummy Gate Patterns"
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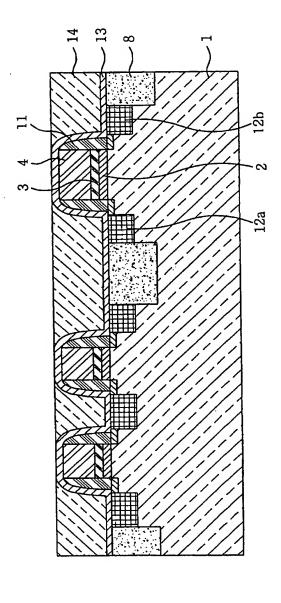
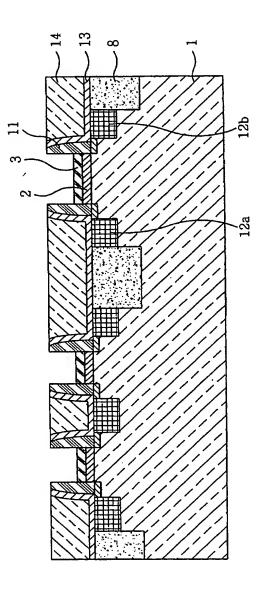


FIG. 1C

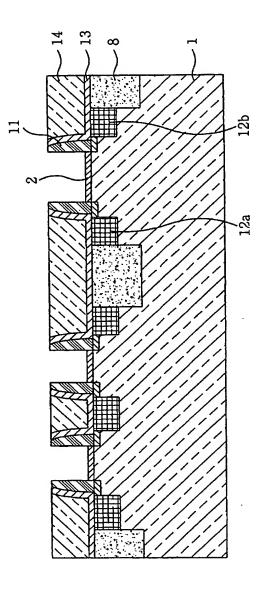
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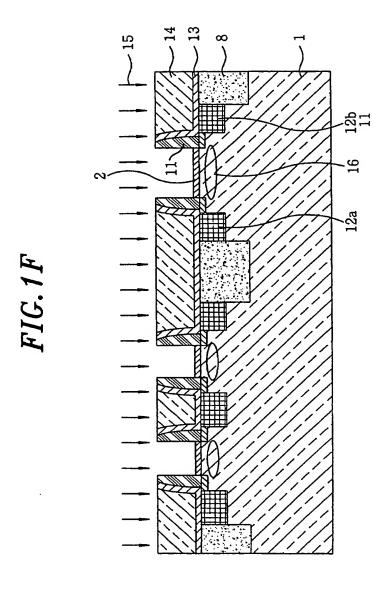


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